

IN THE CLAIMS

Please cancel claim 26 and amend claims 21-23 as set forth below.

21. (Currently Amended) A storage medium on which there is stored a cell library, ~~ahaving~~ logic circuit components , each component having a specific logic function designed in advance, in which there are written at least a function, shape, delay, ~~consumption power and the like~~ and power consumption of each cell,

wherein the cell library is registered with at least two kinds of cells which are different in a delay and ~~consumption~~ power consumption, ~~because of~~ said cells being constructed of switching elements which have different threshold voltages while having the same function and the same shape.

22. (Currently Amended) A designing method for a semiconductor integrated circuit device ~~according to claim 21,~~ using the storage medium on which there is stored the cell library according to claim 21, said method comprising at least the steps of:

calculating ~~consumption power~~ consumption and a delay of a signal path; and

assigning to a logic circuit one cell selected from
among at least two kinds of cells ~~constructed of switching~~
~~elements which have different threshold voltages while having~~
~~the same function and the same shape, using the result of the~~
~~step of calculating consumption power and a delay of a signal~~
~~path~~ registered in said library, while maintaining the same
function and the same shape, based on the result of said
calculated power consumption and delay in said signal path.

23. (Currently Amended) A designing method for a
semiconductor integrated circuit ~~according to claim 21, using~~
the storage medium on which there is stored the cell library
according to claim 21, said method comprising at least the
steps of:

designing a logic circuit using only cells
constructed of switching elements each ~~with~~ element having a
high threshold value;

calculating ~~consumption power~~ consumption and a delay
of a signal path; and

replacing a part of ~~the logic circuit designed using~~
~~only cells constructed of switching elements each with a high~~
~~threshold value, by a cell constructed of switching elements~~
~~each of which has a low threshold value while having the same~~
~~function and the same size~~ said designed logic circuit with
cell(s) selected from said cell library, having low threshold

value(s) while maintaining the same function and the same shape, based on the result of said calculated power consumption and delay in said signal path.

26. (Canceled)